

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	199	triple adj modula\$3 adj redundancy	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/03/20 16:18
S2	199	triple adj modular adj redundancy	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/03/20 16:18
S3	1955	programmable near3 (multiplexer or MUX)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 10:38
S4	3	S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 09:05
S5	17	((("5925920") or ("6617912") or ("6671202") or ("6903571") or ("7023235") or ("6505337") or ("6621296") or ("6621298")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/04/25 10:35
S6	2	("7036059").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/04/25 10:35
S7	204	(programmable near3 (multiplexer or MUX)) with rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:43
S8	88	(programmable near3 (multiplexer or MUX)) near3 rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 10:39
S9	78	unused and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 10:58
S10	1	(unused with duplicat\$3) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 10:59
S11	1	(unused same duplicat\$3) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 10:59

## EAST Search History

S12	20	(unused and duplicat\$3) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:34
S13	18	(state near6 chang\$3) and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:36
S14	56	(state near6 chang\$3) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:44
S15	19133	static adj ram	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:36
S16	22	S15 and S14	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:36
S17	57	(state with chang\$3) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:43
S18	293	(programmable near3 (multiplexer or MUX)) same rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:44
S19	1121	(programmable near3 (multiplexer or MUX)) and rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:46
S20	65	(state near6 chang\$3) and S18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:56
S21	22	S15 and S20	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:45
S22	77	S15 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:45
S23	3	((inadvert\$4 or undesir\$4) near5 state) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:55

## EAST Search History

S24	35	(state near6 chang\$3) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 13:56
S25	313339	(single adj event adj upset) or (state near6 chang\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:46
S26	775	(single adj event adj upset)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:46
S27	1397	((programmable or configurable) near3 (multiplexer or MUX)) and rout\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:47
S28	9	S26 and S27	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:50
S29	360	((static adj3 memory) or sram or (static adj ram)) with (chang\$3 near6 state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:58
S30	6	S27 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:59
S31	7006	((static adj3 memory) or sram or (static adj ram)) with state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:59
S32	104	S27 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/25 14:59
S33	212	seu and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 09:06
S34	57	(mux or (multiplexer or multiplexor)) and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 15:01
S35	50	rout\$3 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 15:02

## EAST Search History

S36	33	plac\$5 and S35	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 13:08
S37	24	((mux or (multiplexer or multiplexor)) with (memory or static or sram)) and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 15:02
S38	21	rout\$3 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/27 15:16
S39	2	("6560743").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/04/27 15:16
S40	16	(US-20010037482-\$ or US-20020095649-\$ or US-20030173993-\$ or US-20040080999-\$).did. or (US-6838899-\$ or US-6127864-\$ or US-6049487-\$ or US-6903571-\$ or US-6211695-\$ or US-5631578-\$ or US-5552721-\$ or US-5598108-\$ or US-6810513-\$ or US-5552722-\$ or US-5867507-\$ or US-5581198-\$).did.	US-PGPUB; USPAT	OR	ON	2007/04/30 08:35
S41	5	seu and S40	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/04/30 08:35

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Results 11 - 20 of about 235 for **seu and sram and mux and routing and PIP**. (0.42 seconds)[\[PDF\] Hardening FPGA-based systems against SEUs: A new design methodology](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)**routing** edges modeling **PIP**, and wiring edges modeling wiring. segments. According to our approach, ... Estimation of **SEU** effects in **SRAM**-based FPGAs", to be ...[www.academypublisher.com/jcp/vol01/no01/jcp01012230.pdf](http://www.academypublisher.com/jcp/vol01/no01/jcp01012230.pdf) - [Similar pages](#)[\[PDF\] An Architecture for Fail-Silent Operation of FPGAs and ...](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)**(PIP)**. (a) local **routing**. PLB. (b) global **routing** (1 PLB) ... "On the Evaluation of **SEU** Sensitiveness in **SRAM**- based FPGAs." Proc. ...[ww1.ucmss.com/books/LFS/CSREA2006/ESA5241.pdf](http://ww1.ucmss.com/books/LFS/CSREA2006/ESA5241.pdf) - [Similar pages](#)[\[PDF\] Chapter 2 RADIATION EFFECTS IN INTEGRATED CIRCUITS](#)

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Single Event Upset (**SEU**) effect in a **SRAM** Memory cell. When a charged particle hits the ... Each output **multiplexer** can select various slice outputs and ...[www.springerlink.com/index/r25277617w717215.pdf](http://www.springerlink.com/index/r25277617w717215.pdf) - [Similar pages](#)[\[PDF\] Soft Error Rate Estimation and Mitigation for SRAM-Based FPGAs](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)Figure 2: An impact of **SEU** on **routing** signals. the number of **SRAM** configuration cells are ... The error list considered in the experiments includes **mux**- ...[www.ece.neu.edu/groups/trg/index\\_files/papers/serfpga/fpga05final.pdf](http://www.ece.neu.edu/groups/trg/index_files/papers/serfpga/fpga05final.pdf) - [Similar pages](#)[\[PDF\] DESIGNING SINGLE EVENT UPSET MITIGATION TECHNIQUES FOR LARGE SRAM ...](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)If A and B disagree due to an **SEU** (or for other reasons), the **MUX** will select ... The main issues of the **SRAM** based FPGAs is the upsets in. the **routing**. ...[www.inf.ufrgs.br/~fglima/lima\\_proposal.pdf](http://www.inf.ufrgs.br/~fglima/lima_proposal.pdf) - [Similar pages](#)[\[PDF\] RHINO 6 month review: SEU Effects](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)**Routing** tools optimize for throughput only. FPGA Tool Flow ... **MUX**. 2.08. SRL. 3.0. INV. 1.0. OR. 1.0. AND. 1.0. BRAM. 59.0. **RAM** ...[rhino.east.isi.edu/papers/FCCM-05-ISI-poster.pdf](http://rhino.east.isi.edu/papers/FCCM-05-ISI-poster.pdf) - [Similar pages](#)[Programmable systems and devices with multiplexer circuits ...](#)Such a situation can occur, for example, when an **SEU** causes a short between two input terminals of a **routing multiplexer**, and the two input terminals are ...[www.freepatentsonline.com/6903571.html](http://www.freepatentsonline.com/6903571.html) - 46k - [Cached](#) - [Similar pages](#)[Methods of reducing the susceptibility of PLD designs to single ...](#)The method of claim 3, wherein the duplicate **routing** paths traverse a programmable **routing multiplexer** in the PLD, and wherein an **SEU** affecting input ...[www.freepatentsonline.com/7111215.html](http://www.freepatentsonline.com/7111215.html) - 85k - [Cached](#) - [Similar pages](#)[\[ More results from www.freepatentsonline.com \]](#)[\[PDF\] ANew Analytical Approach to Estimate the Effects of SEUs in ...](#)

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**multiplexer (MUX)** PIPs, and compound PIPs which consist. in a combination of n cross-point ... that control CLBs and **routing** resources of the **SRAM**-based ...  
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**BABEL: A Glossary of Computer Related Abbreviations and Acronyms**

DRV) DW Data Warehousing DWDM Dense Wavelength Division **Multiplexer** DWG Drawing ... Quality Of Service SQRT Square Root SR Shift Register **SRAM** Shadow Random ...

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